

30V N-ch Power MOSFET, Logic Drive

General Features

- Proprietary New Trench Technology
- $R_{DS(ON),typ.}=1.8m\Omega @V_{GS}=10V$
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

Applications

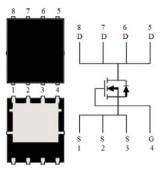
- High efficiency DC/DC Converters
- Synchronous Rectification
- > UPS Inverter

Ordering Information

Part Number	Package	Marking
MXP3003AGL	MaxPAK 5x6	MXP3003AGL

Absolute Maximum Ratings

BV _{DSS}	RDS(ON),max.	D ^[2]	
30V	2.2mΩ	152A	



 $T_{C}\text{=}25\,^{\circ}\!\!\mathrm{C}$ unless otherwise specified

Symbol	Parameter	Value	Unit	
VDSS	Drain-to-Source Voltage ^[1]	30	V	
V _{GSS}	Gate-to-Source Voltage	±20	v	
	Continuous Drain Current ^[2]	152	Α	
ID	Continuous Drain Current ^[3]	130		
	Continuous Drain Current at T _C =100°C	107		
Ідм	Pulsed Drain Current at V _{GS} =10V ^[2,4]	607		
Eas	Single Pulse Avalanche Energy (V _{DD} =30V, V _{GS} =10V, R _G =25Ω, L=1mH)	378	mJ	
Р	Power Dissipation	87	W	
PD	Derating Factor above 25℃	0.6	W/℃	
Τι	Soldering Temperature Distance of 1.6mm from case for 10 seconds	300	Ĉ	
TJ& TSTG	Operating and Storage Temperature Range	-55 to 175	C	

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" may cause permanent damage to the device.

Thermal Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit
Rejc	Thermal Resistance, Junction-to-Case			1.73	℃/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient			72	0.111

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Electrical Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	
BV _{DSS}	Drain-to-Source Breakdown Voltage	30			V	V _{GS} =0V, I _D =250uA	
IDSS	Drain-to-Source Leakage Current			1	uA	V _{DS} =24V, V _{GS} =0V	
				±100	nA	V _{GS} =±20V, V _{DS} =0V	
Igss	Gate-to-Source Leakage Current racteristics			±100		Unless otherwise specified	
Symbol	Parameter	Min.	Typ	Max.	Unit	Test Conditions	
Symbol	Faranieter		Тур.				
RDS(ON)	Static Drain-to-Source		1.8	2.2	mΩ	V _{GS} =10V, I _D =80A ^[5]	
100(011)	On-Resistance ^[4]		2.4	3.1	mΩ	V_{GS} =4.5V, I _D =80A ^[5]	
V _{GS(TH)}	Gate Threshold Voltage	1.0		3.0	V	V _{DS} = V _{GS} , I _D =250uA	
Dynami	c Characteristics	Es	sentially	independ	lent of op	erating temperature	
Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	
Ciss	Input Capacitance		3.6			V _{GS} =0V, V _{DS} =25V, f=1.0MHz	
Crss	Reverse Transfer Capacitance		0.34		nF		
Coss	Output Capacitance		0.75				
Rg	Gate Series Resistance		1.6		Ω	f=1.0MHz	
Qg	Total Gate Charge		36			V _{DD} =15V, I _D =80A, V _{GS} =4.5V	
Qgs	Gate-to-Source Charge		12		nC		
Q _{gd}	Gate-to-Drain (Miller) Charge		14			10-60A, VGS-4.5V	
Resistiv	e Switching Characteristics		Esser	tially inde	ependent	of operating temperature	
Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Conditions	
t _{d(on)}	Turn-on Delay Time		18			\/ _1 5\/	
t _{rise}	Rise Time		5			V _{DD} =15V I _D =80A V _{GS} =4.5V	
t _{d(off)}	Turn-off Delay Time		68		ns		
t _{fall}	Fall Time		13			R _G =2.5Ω	
Source-	Drain Body Diode Characteristic	cs	L	L		unless otherwise specified	
Symbol	Parameter	Min	Тур.	Max.	Unit	Test Conditions	
Isd	Continuous Source Current ^[2]			152	А	Maximum Ratings	
V _{SD}	Diode Forward Voltage		0.9	1.2	V	I _S =80A, V _{GS} =0V	
trr	Reverse Recovery Time		42		ns	V _{GS} =0V	
Qrr	Reverse Recovery Charge	1	4.6	1	nC	I⊧=20A,di/dt=100A/µs	

Note:

[1] TJ=+25℃ to +175℃

[2] Silicon limited current only

[3] Package limited current

[4] Repetitive rating, pulse width limited by both maximum junction temperature.

[5] Pulse width≤380µs; duty cycle≤2%.



Power Dissipation (W)

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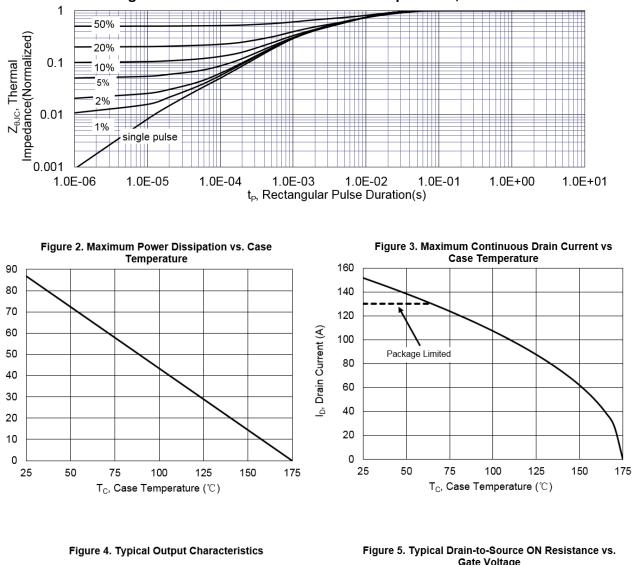
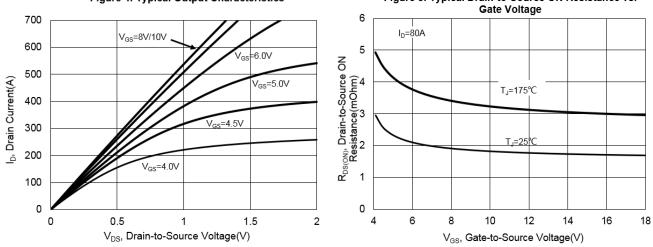


Figure 1. Maximum Effective Thermal Impedance, Junction-to-Case



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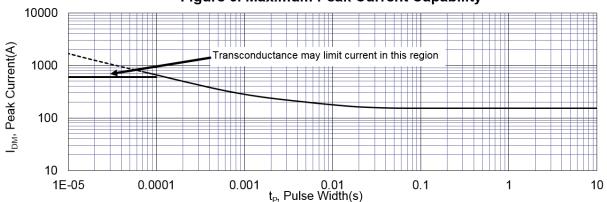


Figure 6. Maximum Peak Current Capability

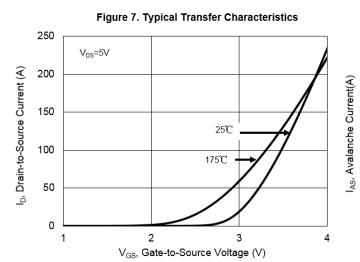
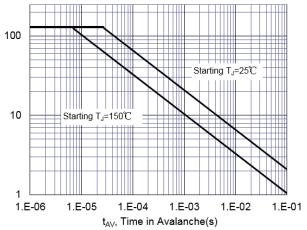
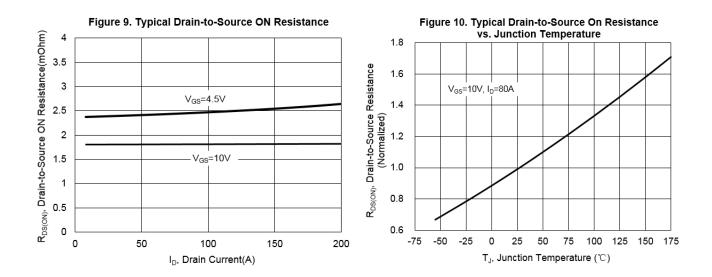


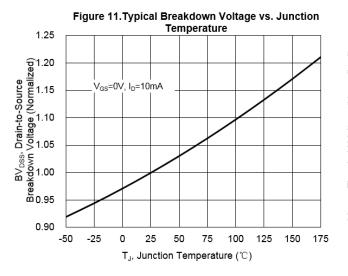
Figure 8. Unclamped Inductive Switching Capability





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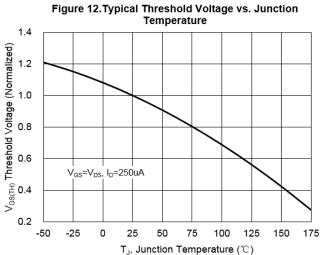


Figure 13. Maximum Forward Safe Operation Area

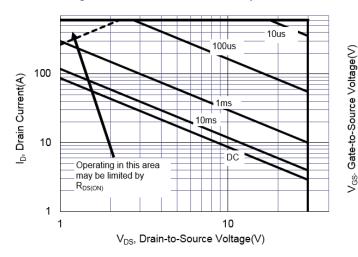
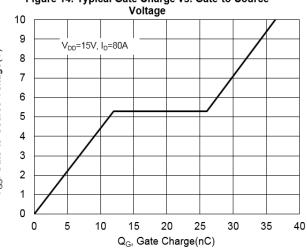
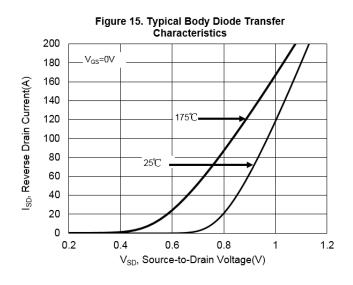


Figure 14. Typical Gate Charge vs. Gate-to-Source

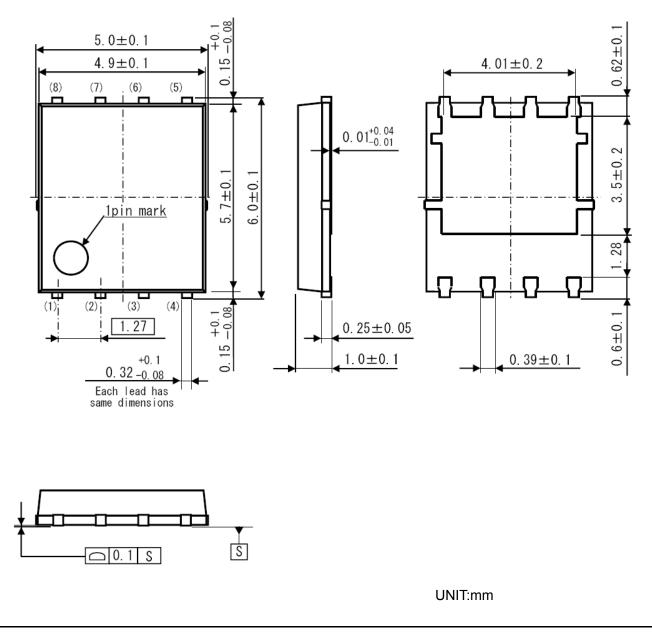




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MaxPAK 5x6





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